



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

149/Response
4-14-03
C Moore

Applicant : Vickram R. Vathulya
Title of Invention : CIRCUIT AND METHOD FOR INPUT SIDE
IMPEDANCE MATCHING OF A POWER
AMPLIFIER IN AN ELECTRONIC DEVICE
Date Filed : March 19, 2001
Serial No. : 09/811,647
Examiner : BETTENDORF, JUSTIN P
Art Unit : 2817
Attorney Docket No. : US 010082

RECEIVED
APR 11 2003
TECHNOLOGY CENTER 2800

Box RCE
Commissioner for Patents
Washington, DC 20231

AMENDMENT

In the Drawings:

The applicant has amended Figures 1 and 2 by adding a legend "Prior Art". A proposed drawing correction with the added legend in red is enclosed for approval, and a corrected version is also enclosed for replacement upon approval.

REMARKS

This RCE is responsive to the final Office Action dated January 7, 2003 in which the Examiner rejects all the pending claims 1- 20 as either being anticipated by Utsu et al (US Patent No. 5,343,172) under 35 USC §102(b) or as being obvious over Utsu et al, or as being obvious over Utsu et al in view of Lleveland et al (WO 98/47190) under 35 USC §103(a). The Examiner also objects